

FIG.1

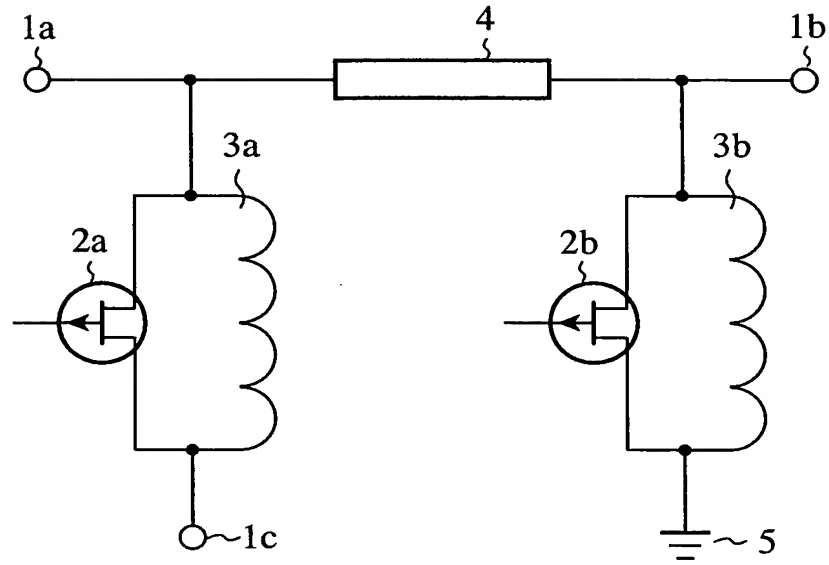


FIG.2

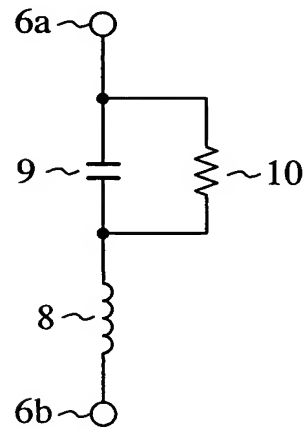


FIG.3



FIG.4

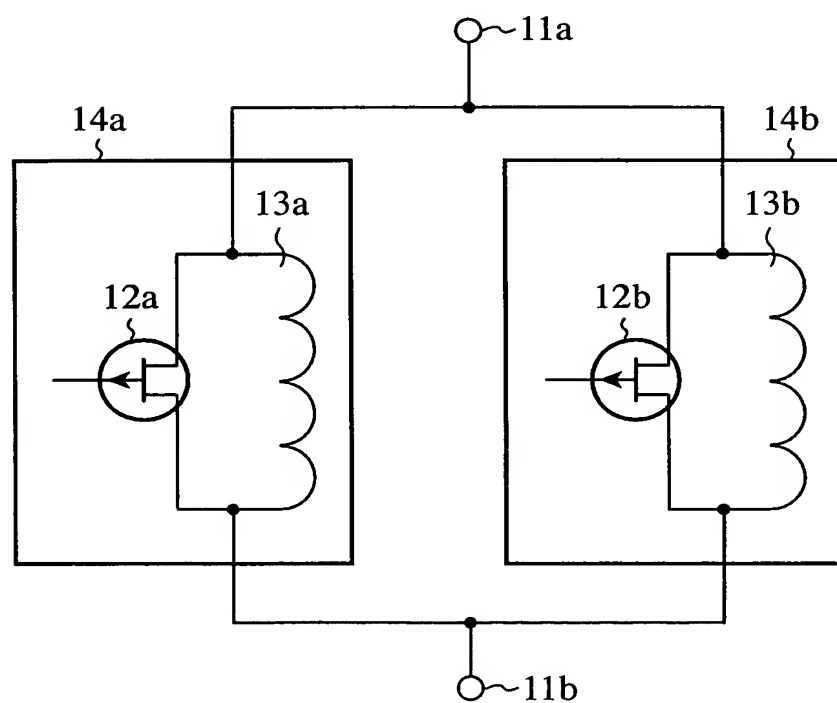


FIG.5

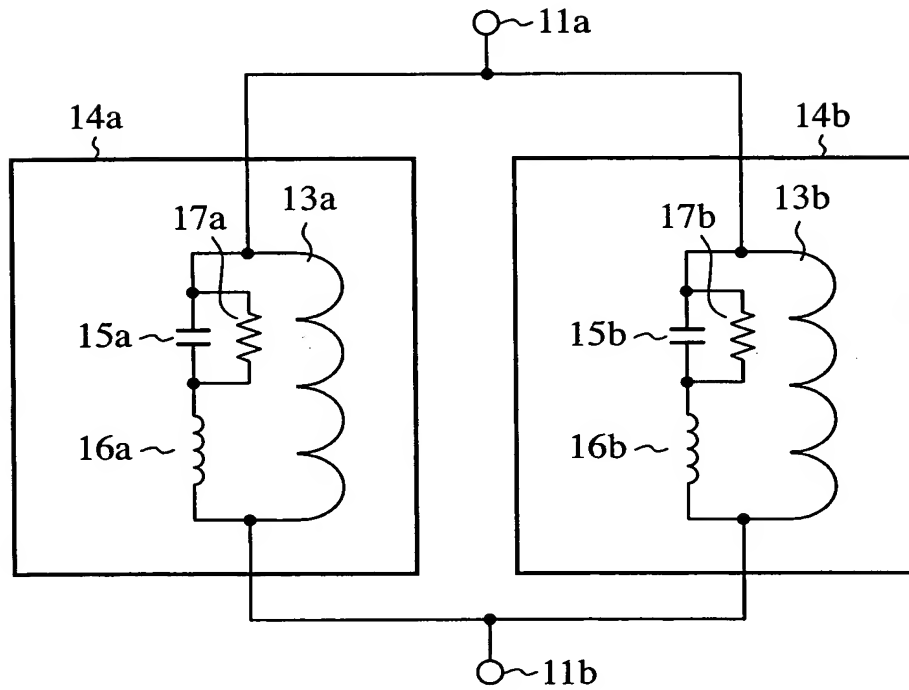


FIG.6

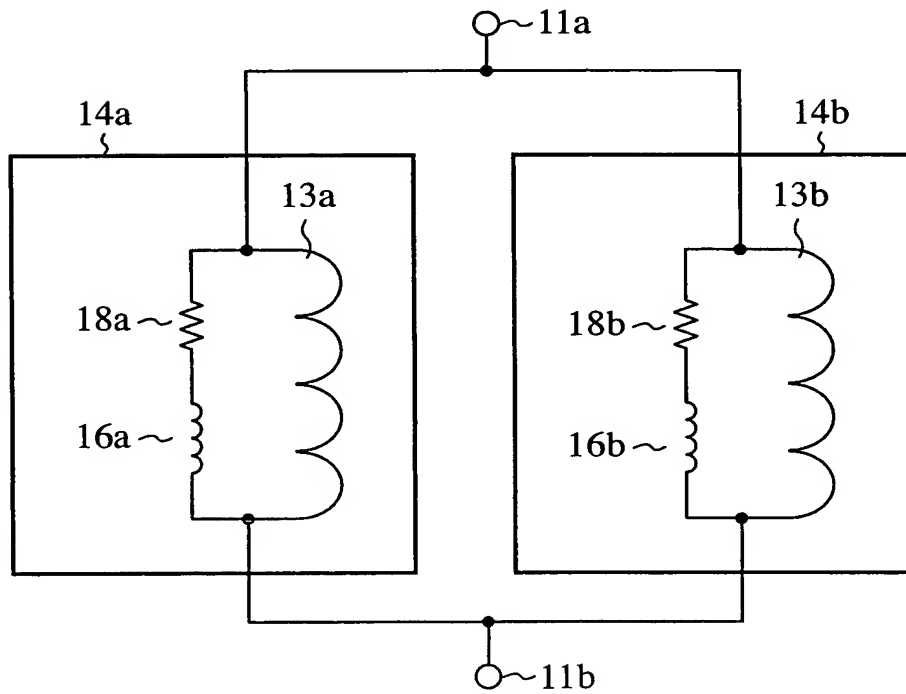


FIG.7

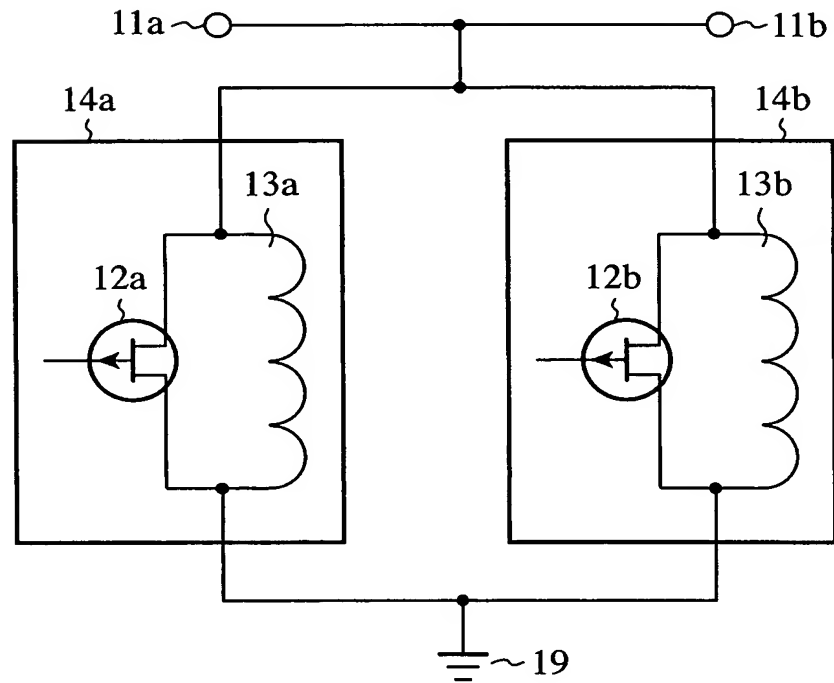


FIG.8

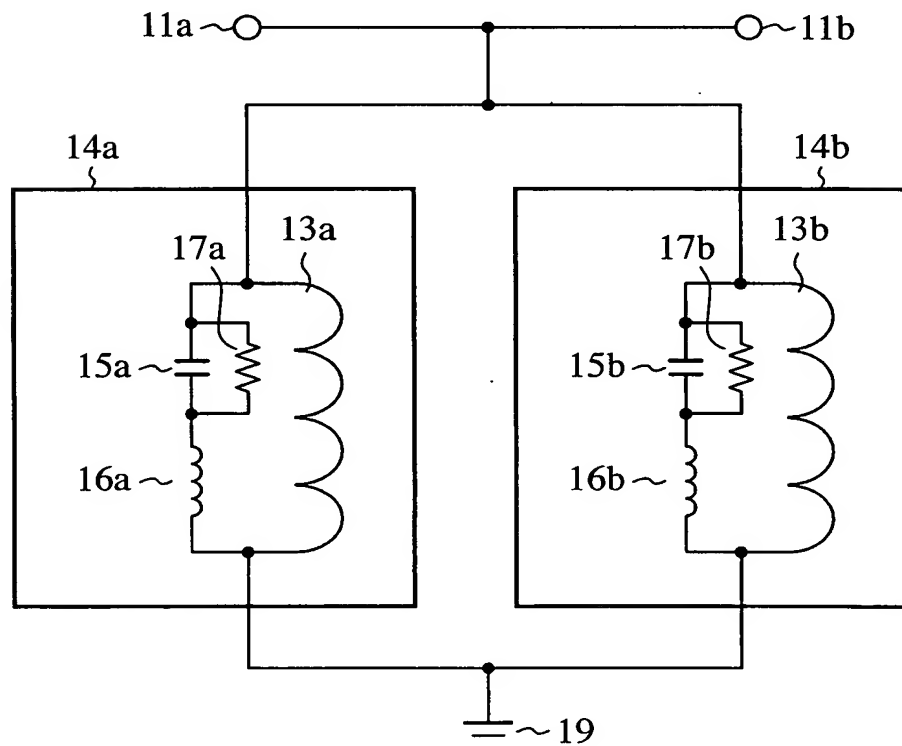


FIG.9

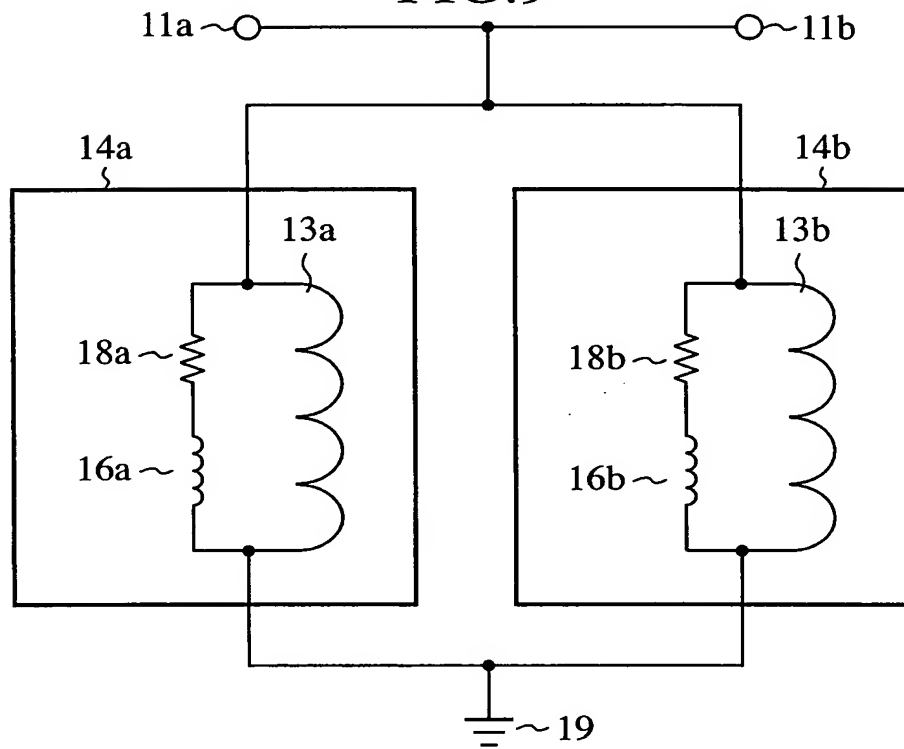


FIG.10

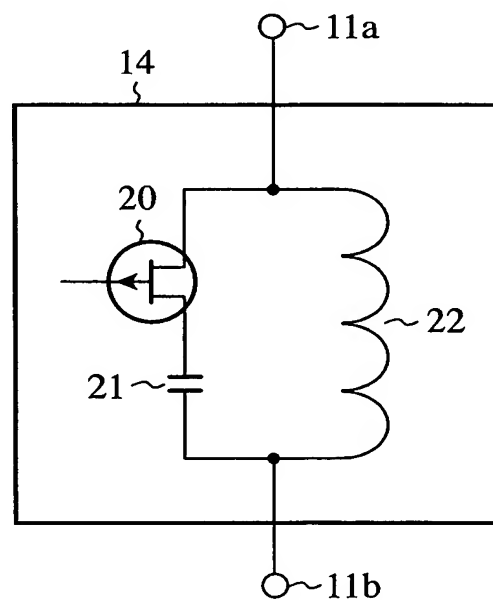


FIG.11

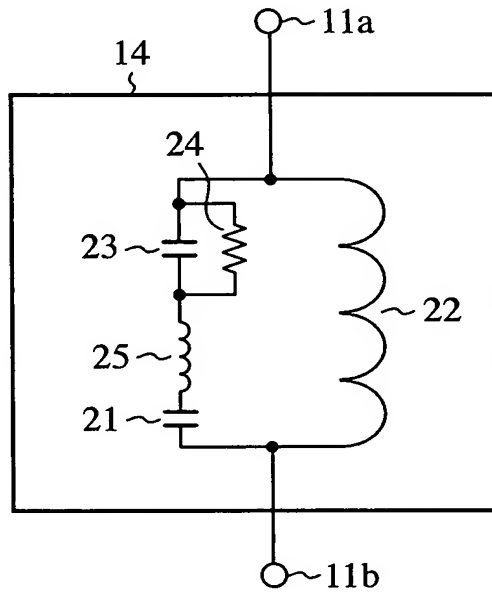


FIG.12

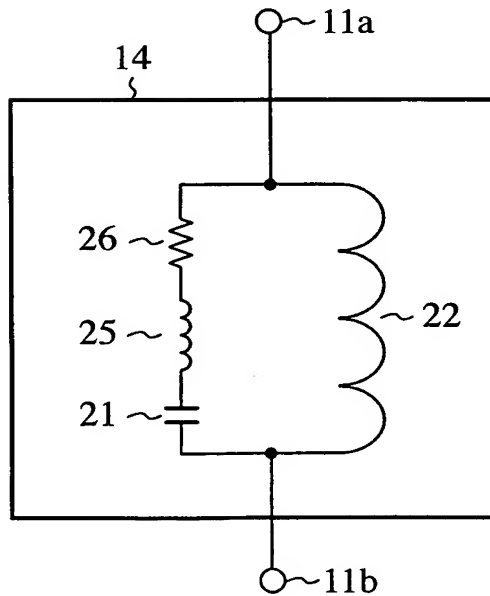


FIG.13

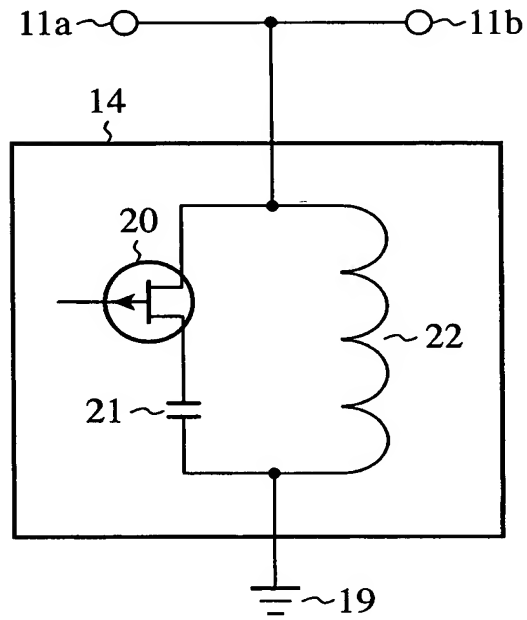


FIG.14

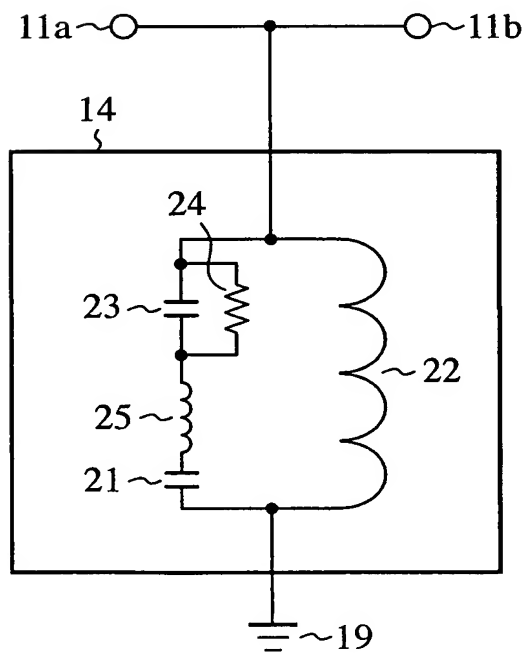


FIG.15

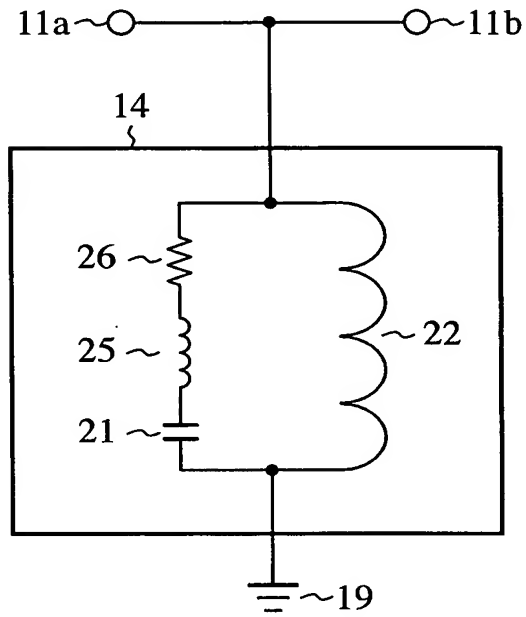


FIG.16

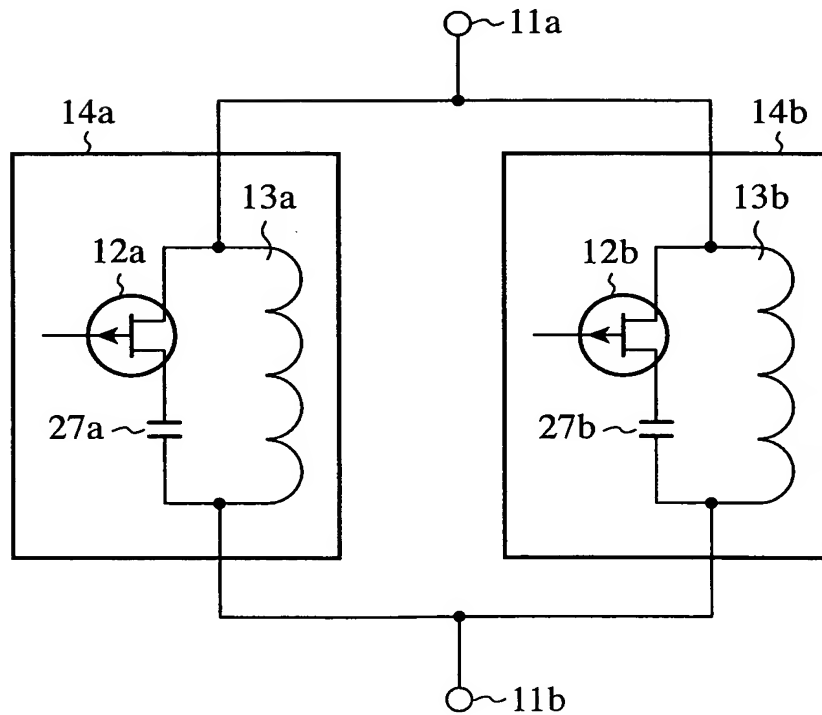


FIG.17

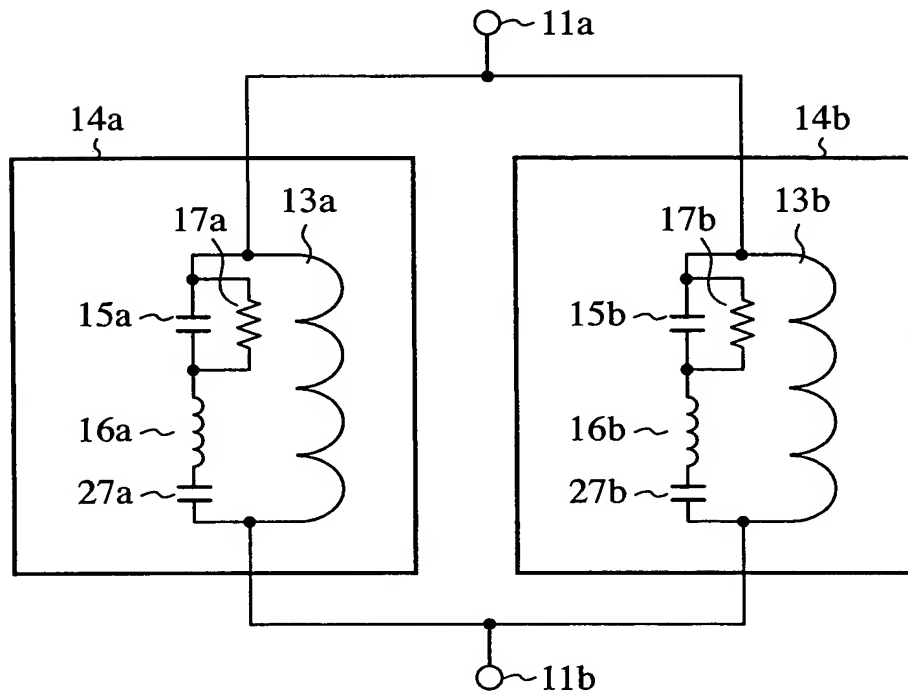


FIG.18

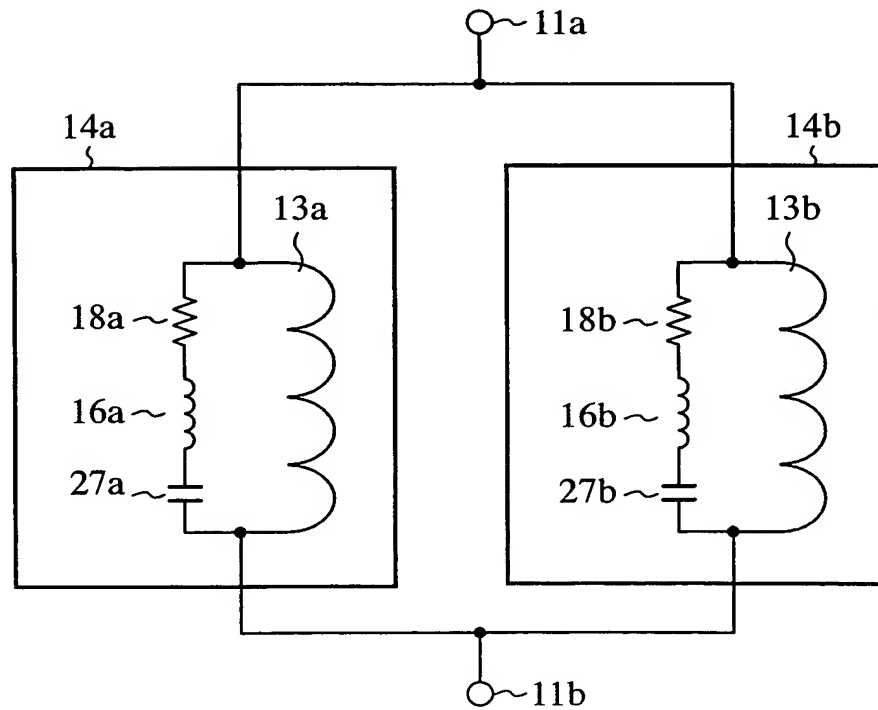


FIG.19

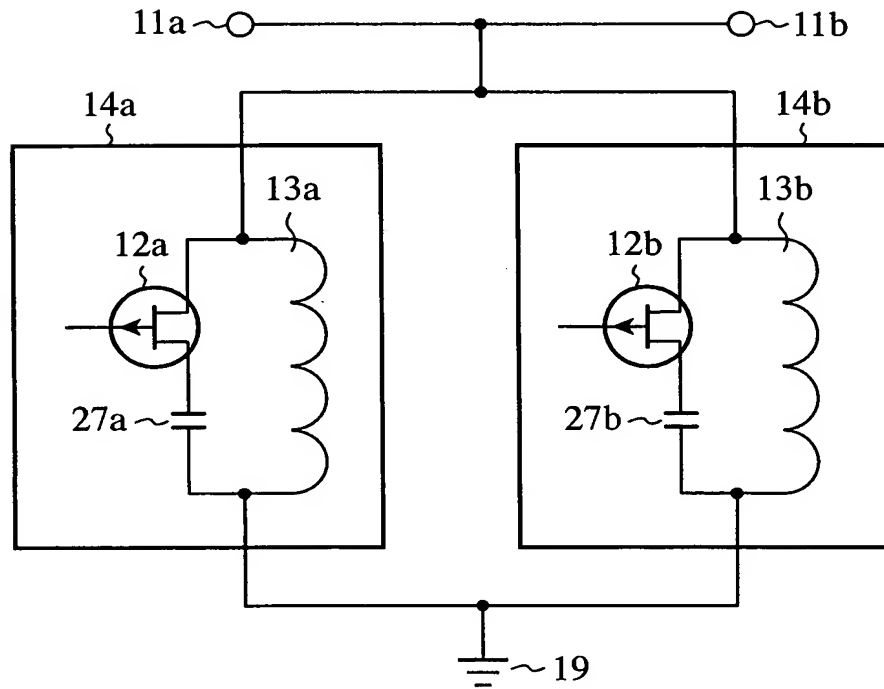


FIG.20

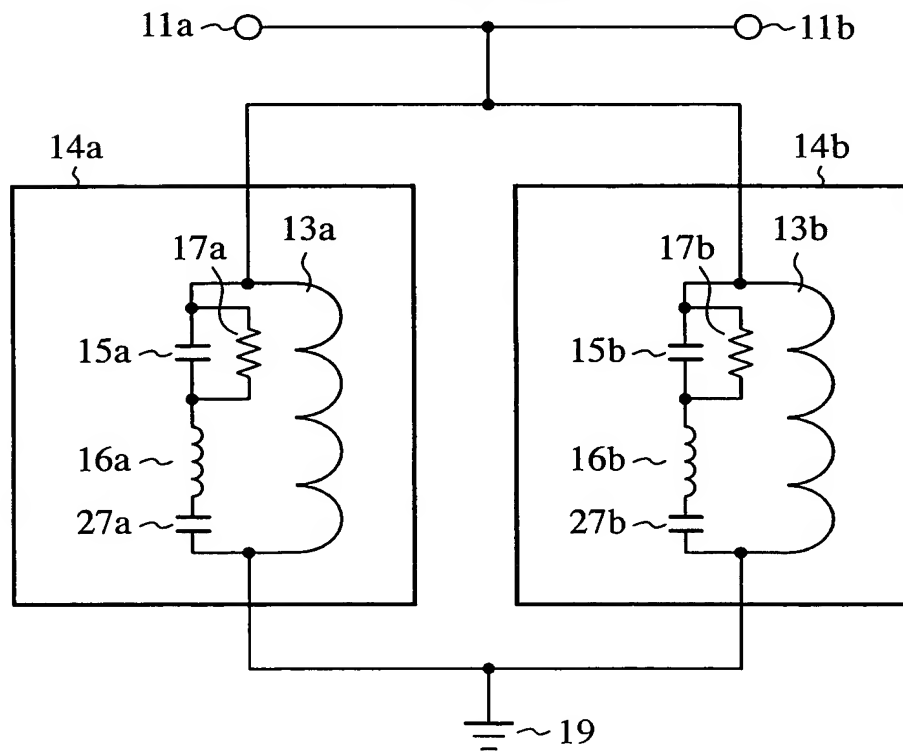


FIG.21

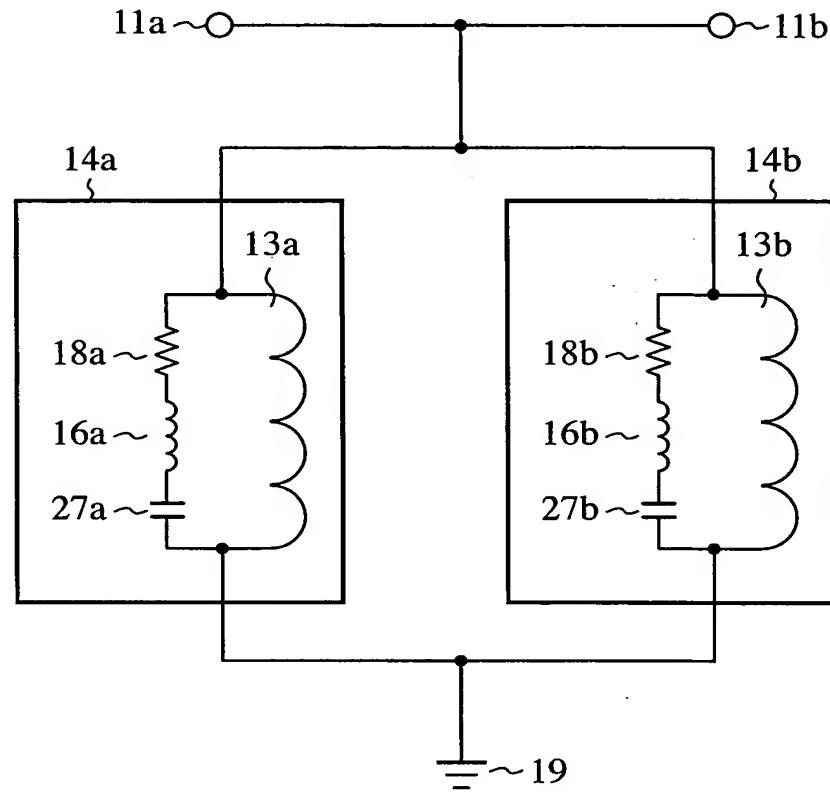


FIG.22

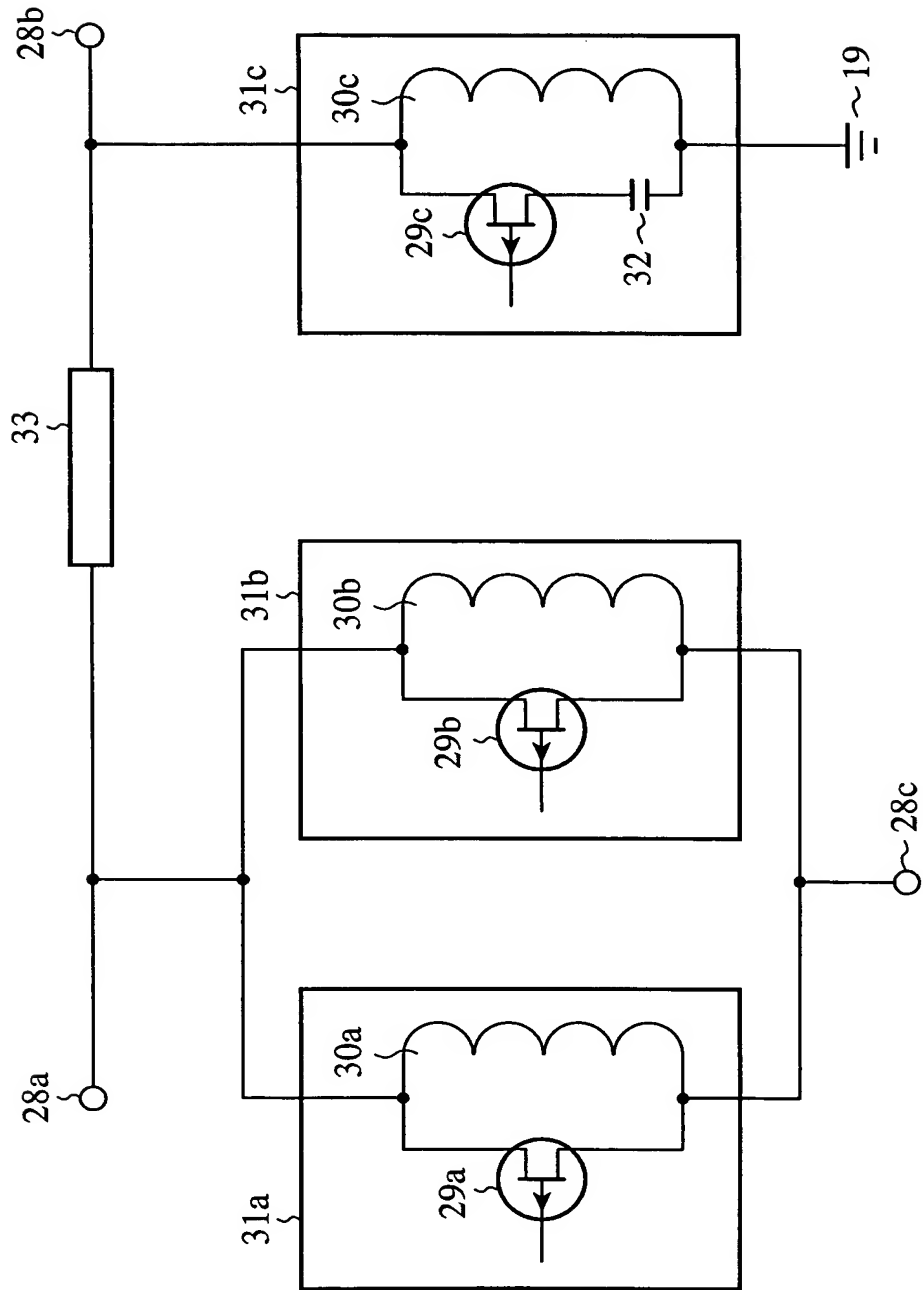


FIG.23

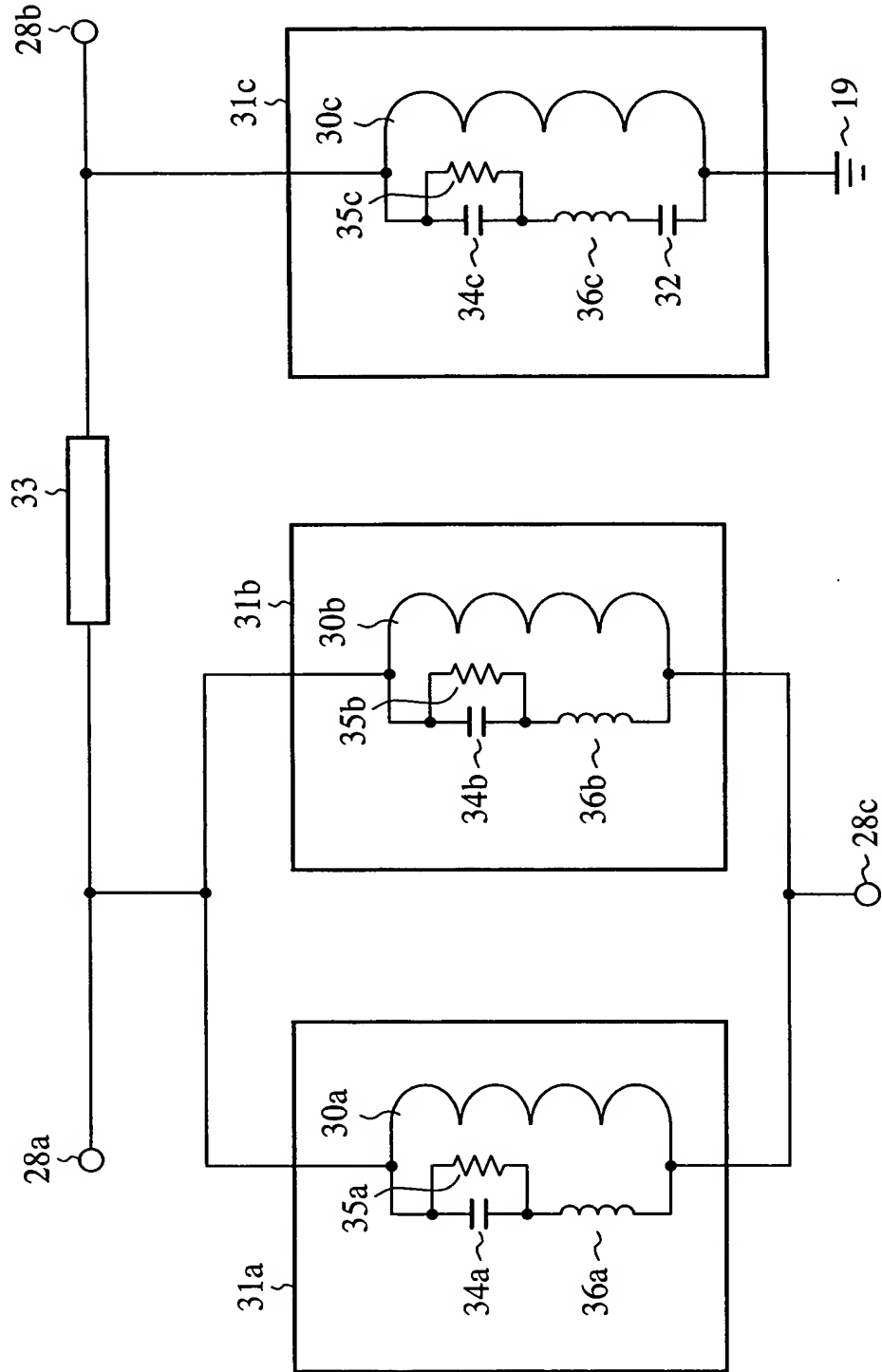


FIG.24

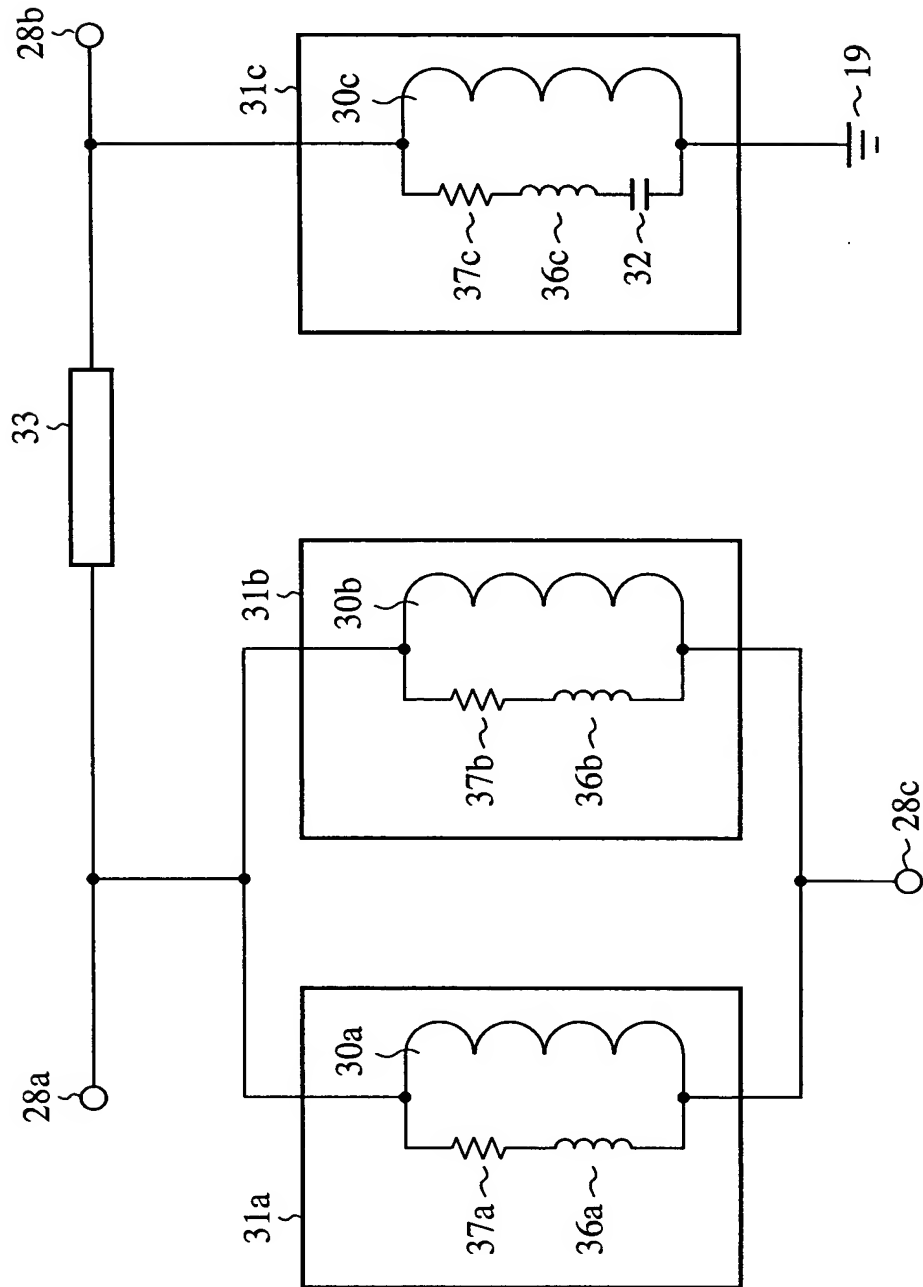


FIG.25

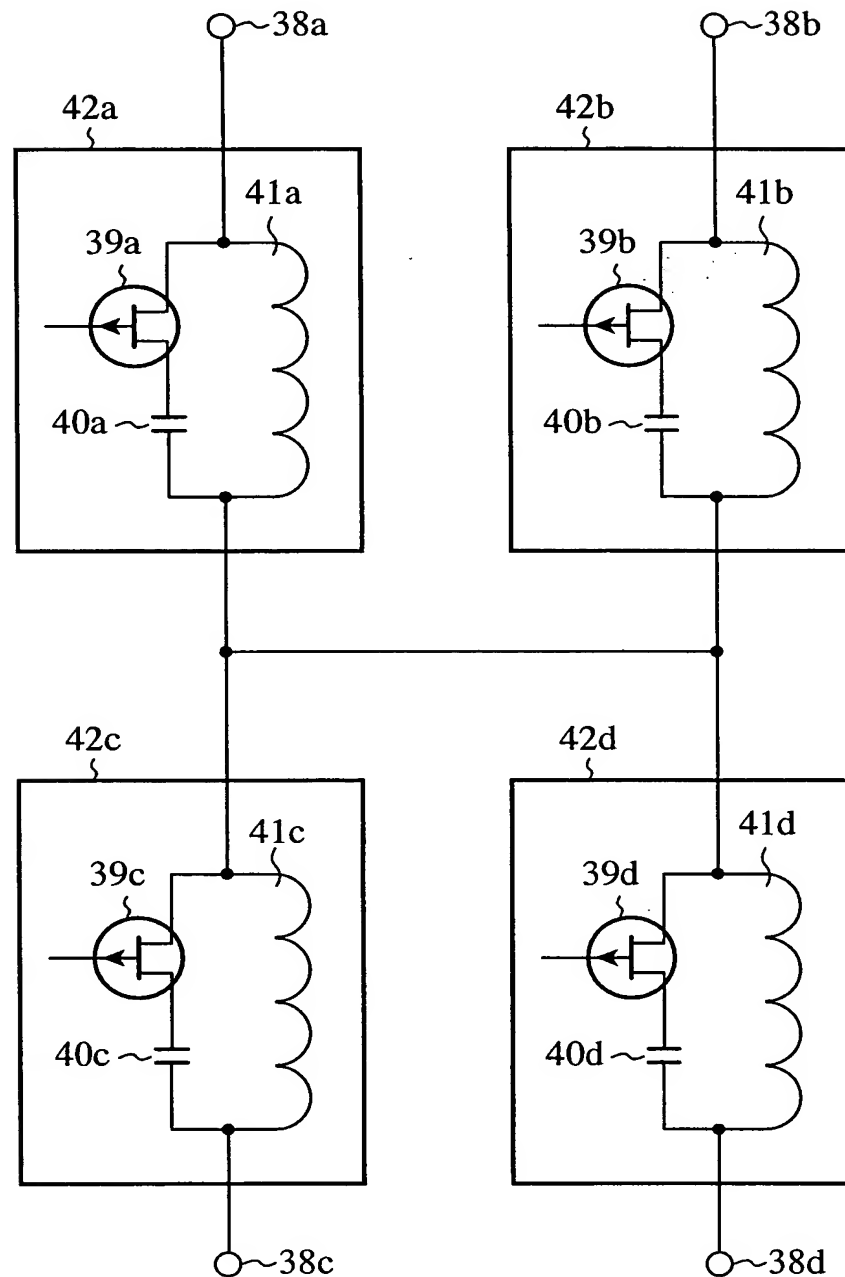


FIG.26

| Input terminal or output terminal | Output terminal or input terminal | FET39a | FET39b | FET39c | FET39d |
|-----------------------------------|-----------------------------------|--------|--------|--------|--------|
| 38a | 38b | ON | ON | OFF | OFF |
| | 38c | ON | OFF | ON | OFF |
| | 38d | ON | OFF | OFF | ON |
| 38b | 38c | OFF | ON | ON | OFF |
| | 38d | OFF | ON | OFF | ON |
| 38c | 38d | OFF | OFF | ON | ON |